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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Application No.: 09/976,708

Filed: October 12, 2001

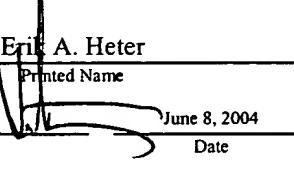
Inventor(s):

Michael C. Dorsey

§ Examiner: Trimmings, J.
§ Group/Art Unit: 2133
§ Atty. Dkt. No: 5681-55500

Title: ASIC BIST
CONTROLLER
EMPLOYING MULTIPLE
CLOCK DOMAINS

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

 Erik A. Heter
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 June 8, 2004
Signature Date

AMENDMENT; RESPONSE TO OFFICE ACTION OF **RECEIVED**
March 8, 2004

JUN 17 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Technology Center 2100

Dear Sir:

This paper is submitted in response to the Office Action of March 8, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below. A listing of the drawing changes begins on page 2. Amendments to the specification begin on page 3. A listing of the claims, including any amendments, begins on page 6. Remarks begin on page 13.